# CURRICULUM VITAE

# Aatira Ramesh

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**Objective**:

 To attain success in whatever I do through hard work and patience.

**Academic Qualifications**:

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| --- | --- | --- | --- |
| **Course** | **University** | **Year of Passing** | **Marks Obtained** |
| M Tech (VLSI Design) | GITAM University | 2013 | 8.00 CGPA |
| BE (Electronics & Telecommunication) | Pune University | 2007 | 60% |
| 12th | CBSE | 2002 | 88.8% |
| 10th | CBSE | 2000 | 88.6% |

**Working & Training Experience**:

* Overall 2 years experience in IT firm Syntel Ltd. Chennai(Nov2007-Feb 2010)
* Worked as Assistant Professor in Dadi Institute of Engineering & Technology(DIET).

**M.Tech Project:**

**Differential Amplifier Circuit Optimization using Particle Swarm Optimization (PSO)**

A differential amplifier circuit is optimized for minimum area and power dissipation using PSO Algorithm.

**Technical Seminars Presented**:

* GPS- A space-based global navigation satellite system
* NRZ to Manchester Code Convertor
* Wien-Bridge Oscillator- Implementation and Applications
* DCVSL – Differential Cascode Voltage Switch Logic

**Additional Tools**:

* WEBFOCUS, MATLAB, Verilog, VHDL, TopSpice

**Personal Details**:

Nationality : Indian

Date of Birth : 25 Apr 85

Sex : Female

Marital Status : Married

Languages Known : English, Hindi & Malayalam

### Passport : Held (Valid upto 13/05/2018)

**Present & Permanent Address**

“Gangothri”, Grace Garden Housing Colony

D/o Col. G.Ramesh (Redt.)

Thrikkakara (PO), Ernakulam(Dist)

Kerala, PIN: 682028

**Reference**

1. Mr. Suresh Krishnasamy

Sr. Programmer

Syntel International Pvt. Ltd.

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2. Mr. Vamsidhar Anagani

 Head of the Department

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